

KWSRC0524

Features

- 5A Peak Pulse Current($t_p=8/20\mu s$)
- Replacement for DFN-10L(2.5x1.0x0.58mm)
- Protects two I/O and one power line
- Low Clamping Voltage
- Reverse Voltage:5V
- Low Leakage Current
- Response Time is Typically $<1\text{ ns}$

Mechanical Characteristics

- JEDEC DFN-10L package
- ROHS Compliant
- Marking:Marking Code

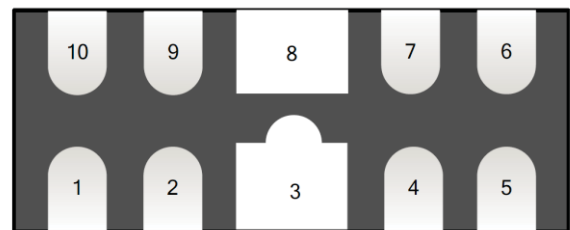
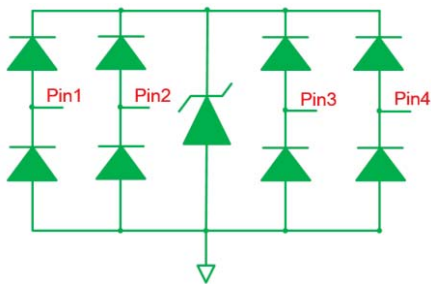
IEC Compatibility(EN61000-4)

- IEC61000-4-2(ESD) $\pm 15\text{ kV}$ (air), $\pm 8\text{ kV}$ (contact)
- IEC61000-4-4(EFT)40A(5/50ns)
- IEC61000-4-5(Surge)5A(8/20 μs)

Applications

- Cellular Handsets and Accessories
- Digital Visual Interface(DVI)
- PCI Express
- Set-Top Box
- Servers, Notebook, and Desktop PC
- HDMI Ports

Schematic & Pin Configuration



DFN-10L Top View

Absolute Maximum Rating

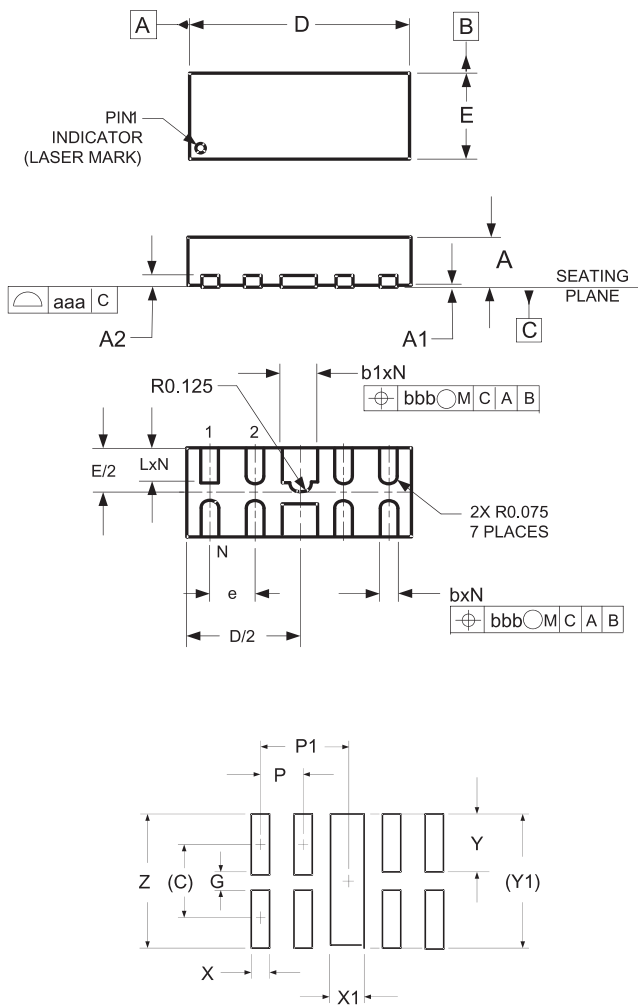
Rating	Symbol	Value	Units
Peak Pulse Power($t_p=8/20\mu s$)	PPK	150	Watts
Lead Soldering Temperature	TL	260(10 sec.)	$^{\circ}\text{C}/\text{W}$
Operating Temperature	TJ	-55-150	$^{\circ}\text{C}$
Storage Temperature	TSTG	-40-120	$^{\circ}\text{C}$
IEC61000-4-2	VPP	Air 15 Contact 8	KV

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Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_t=1mA$	6			V
Leakage Current	I_R	@ V_{RWM}			3	μA
Clamping Voltage	V_C	@1A			9.8	V
Peak Pulse Current	I_{PP}	8/20 μs			5	A
Junction Capacitance	C _{I/O-GND}	$V_R=0V, f=1MHz$		0.8		pf
	C _{I/O-I/O}	$V_R=0V, f=1MHz$		0.4		pf

Outline Drawing-DFN-10L



DIMENSIONS						
DIM	Inches			Millimeters		
	Min	Nom	Max	Min	Nom	Max
A	.020	.023	.026	0.50	0.58	0.65
A1	.000	.001	.002	0.00	0.03	0.05
A2	(0.005)			(0.13)		
b	.006	.008	.010	0.15	0.20	0.25
b1	.014	.016	.018	0.35	0.40	0.45
D	.094	.098	.102	2.40	2.50	2.60
E	.035	.039	.043	0.90	1.00	1.10
e	.020 BSC			0.50BSC		
L	.012	.015	.017	0.30	0.38	0.425
N	8			8		
aaa	0.003			0.08		
bbb	0.004			0.10		

DIMENSIONS		
DIM	Inches	Millimeters
C	(.034)	(0.875)
G	.008	0.20
P	.020	0.50
P1	.039	1.00
X	.008	0.20
X1	.016	0.40
Y	.027	0.675
Y1	(.061)	(1.55)
Z	.061	1.55